

**PATENT APPLICATION TRANSMITTAL LETTER**

(Large Entity)

Docket No.  
INTL-0289-US (P7820)**TO THE ASSISTANT COMMISSIONER FOR PATENTS**

Transmitted herewith for filing under 35 U.S.C. 111 and 37 C.F.R. 1.53 is the patent application of:

**ANDY P. STANLEY****For: AUTOMATICALLY ENABLING INFORMATION TO BE DISPLAYED AFTER A PROCESSOR-BASED  
SYSTEM IS TURNED OFF**

Enclosed are:

- ☒ Certificate of Mailing with Express Mail Mailing Label No. **EL515089100US**  
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☐ A certified copy of a \_\_\_\_\_ application.  
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**CLAIMS AS FILED**

For	#Filed	#Allowed	#Extra	Rate	Fee
<b>Total Claims</b>	20	- 20 =	0	x \$18.00	\$0.00
<b>Indep. Claims</b>	3	- 3 =	0	x \$78.00	\$0.00
<b>Multiple Dependent Claims (check if applicable)</b> <input type="checkbox"/>					\$0.00
<b>BASIC FEE</b>					\$760.00
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Dated: **November 29, 1999**  
Signature

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**APPLICATION**

**FOR**

**UNITED STATES LETTERS PATENT**

**TITLE:**           **AUTOMATICALLY ENABLING INFORMATION TO  
BE DISPLAYED AFTER A PROCESSOR-BASED  
SYSTEM IS TURNED OFF**

**INVENTORS:**     **RANDY P. STANLEY**

Express Mail No.: EL515089100US

Date: November 29, 1999

Patent Application

AUTOMATICALLY ENABLING INFORMATION TO BE DISPLAYED  
AFTER A PROCESSOR-BASED SYSTEM IS TURNED OFF

Background

This invention relates generally to processor-based systems and in some particular embodiments to processor-based systems which maintain time sensitive information  
5 such as scheduling information for the user.

A personal information manager (PIM) application such as Microsoft Outlook, Lotus Organizer and Sidekick may provide a calendar, notes, a to do list, electronic mail and so forth. Much of this information is time sensitive.  
10 For example, an alert may be programmed to signal the user at a particular time to take a certain course of action, such as to attend a meeting or to make a phone call. The alert may be accompanied by a display which displays the time sensitive act.

15 One problem with such systems is that if the user turns his or her processor-based system off, the user will miss the notification. Thus, conventionally, turning the computer off disables the system from providing the predetermined notice at the predetermined time.

20 Of course, the processor-based system user may overcome this problem by simply leaving his or her computer always on. However, this practice is not recommended and may greatly drain the power resources of the processor-

based system. Thus, conventionally users turn their processor-based systems off when not being used. When they do so, they risk the possibility that they will miss important alerts.

5           Thus, there is a need for ways which enable a user to be advised of time sensitive information when the user's personal computer is in a powered-off state.

#### Summary

10           In accordance with one aspect, a method includes automatically transferring time sensitive data from a storage coupled to a first processor-based device to a storage coupled to the second processor-based device. The time sensitive data is automatically displayed on a display coupled to a second processor-based device at a  
15           predetermined time.

Other aspects are set forth in the accompanying detailed description and claims.

#### Brief Description of the Drawings

20           Figure 1 is a schematic depiction of one embodiment of the present invention;

Figure 2 is a flow chart showing software in accordance with one embodiment of the present invention which may be provided as part of the processor-based system shown in Figure 1;

Figure 3 is a flow chart showing software which may be run by the standby system shown in Figure 1;

Figure 4 is a perspective view of one embodiment of the present invention;

5        Figure 5 is a block diagram of the standby system shown in Figure 1 in accordance with one embodiment of the present invention;

Figure 6 is a graphical user interface in accordance with one embodiment of the present invention; and

10       Figure 7 is a block diagram for one embodiment of the processor-based system 10, shown in Figure 1.

#### Detailed Description

Referring to Figure 1, a processor-based system 10 may be coupled to a standby system 12. In one embodiment of  
15       the present invention, the processor-based system 10 and the standby system 12 may be contained within the same housing. Thus, the user may not appreciate that two distinct processor-based systems are provided.

The processor-based system 10 may be any conventional  
20       computer system, including a laptop or portable computer system operable from a battery. The processor-based system 10 may include a real time clock (RTC) 14 and an information manager application 16 such as a personal information manager (PIM) application. A driver 20 may  
25       drive a speaker 22 in accordance with one embodiment of the present invention. An application program 24 may act as an

interface between the application 16 and the standby system 12.

While the present invention is described in connection with an embodiment in which a personal information management (PIM) application utilized, the present application is applicable to systems which involve time sensitive data which may come in a variety of different types of information including time information and time sensitive alerts as additional examples. Thus, it is not essential that the information be associated with a PIM application in particular.

The standby system 12 may be coupled to the processor-based system 10 by an appropriate link. In embodiments in which separate housings are utilized for the systems 10 and 12, a tethered connection may be provided between the systems 10 and 12. In other embodiments, the systems 10 and 12 may be coupled by a airwave communication link, such as a infrared link, a radio link or a cellular telephone link.

In some embodiments of the present invention, the standby system 12 may use the same power supply as the system 12. In the case where the processor-based system 10 is run from a battery, the standby system 12 may also be operated from the same battery. In addition, in some embodiments, the standby system 12 may be coupled to the

real time clock 14 of the processor-based system 10 so that timing is synchronized between the two systems.

The processor-based system 10 may store an application 16, such as a scheduler, a calendar or the like which may receive time sensitive data such as the time for appointments, telephone calls or the like. The application 16 may be user programmed to give a visible or audible alert at a preprogrammed time. In order to ensure that this information is always available for notification to the user, the application 16 information may be transferred to the standby system 12.

This transfer of application 16 information may be implemented in a variety of ways. It may be implemented automatically in response to an indication that the processor-based system 10 is about to be powered off. Alternatively, every time a given type of information is stored on the processor-based system 10 in association with the application 16, that information may be automatically transferred to the standby system 12. For example, whenever the user sets an alert to audibly or visibly notify the user of a given event, information about that alert may automatically be transferred to the standby system 12. As still another embodiment, the information associated with the application 16 may automatically be transferred at periodic intervals to the standby system 12.

In each case, the expectation is that by automatically transferring the time sensitive data from the processor-based system 10 to the system 12, upon power off of the system 10, the data may still be available on the system 12. The system 12 may include a display 18 in one embodiment of the present invention that may be operated even when the processor-based system 10 is in its power off state. In addition, the standby system 12 may operate the driver 20 and speaker 22 to provide an audible indication, at a predetermined time, in keeping with the information provided by the application 16. In other embodiments of the present invention, the standby system 12 may include its own speaker 22.

Through the standby system 12 that is always powered on, the user may be notified of an important activity or event that is stored in his or her application 16. Alternatively, the system 12 may be automatically powered on whenever the processor-based system 10 is about to be powered off. Thus, one of the two systems 10 or 12 is always in a powered on state ready to provide time sensitive data, in one embodiment of the invention.

Referring to Figure 2, in accordance with one embodiment of the present invention, software 24 may be stored on the processor-based system 10, for example a hard disk drive. The flow begins by determining when a power off state is about to occur as indicated in diamond 26. In



such case, any active application 16 task may be transferred to the standby system 12 as indicated in block 28. Thus, events of a particular type may automatically be transferred from the system 10 to the system 12 prior to shut down of the system 10. The software 24 may extract time sensitive alerts from PIM applications in one embodiment of the invention.

For example, pre-programmed alerts which are designed to notify the user of a given event may be transferred to the standby system 12 prior to shut off. After transferring the information, the processor-based system 10 may proceed to a power off state, as indicated in block 30.

In other embodiments of the present invention, as described previously, the transfer of time sensitive data to the standby system 12 may be done automatically whenever events of a certain type are preprogrammed. Alternatively, the data may be periodically automatically transferred to the standby system 12.

In some embodiments of the present invention, the power consumption of the standby system 12 may be considerably less than that encountered with the overall system 10. Thus, power may be reasonably conserved.

Turning now to Figure 3, software 32 may be stored on the standby system 12 in accordance with one embodiment of the present invention. The software 32 may monitor for PIM application 16 information, as indicated in diamond 34.

When PIM information or other time sensitive data is transferred, the standby system 12 may be activated automatically. The system 12 may compare the time of the time sensitive data, such as a PIM alert, contained in a queue containing one or more time sensitive events, to the information about the current time from the real time clock 14, as indicated in block 36. When there is a match, as indicated in diamond 38, an audio or visible display may be activated as indicated in block 40. In one such case, an image of the calendar, produced by the PIM application 16, may be displayed on a display 18 or a sound may be produced, for example from the speaker 22, to alert the user (block 42). After a time out is reached, as indicated in diamond 44, the flow may be terminated.

Turning next to Figure 4, in accordance with one embodiment of the present invention, the system 10 may be a laptop or portable computer which includes within its housing the system 10 and the standby system 12. The laptop may include a display portion 46 that folds onto a keyboard portion 48. The display 18 may be provided on the exterior of the housing of the system 10, for example on the portion 46. Thus, even though the system 10 is in its closed configuration and is powered off, the user may be notified, for example through the display 18 on the exterior of the housing, of alerts and other time sensitive events.

In accordance with one embodiment of the present invention, the standby system 12 may be implemented by a microcontroller 50 which is coupled to the display 18 and to a storage 52, as shown in Fig. 5. For example, in one  
5 embodiment of the present invention, the storage 52 may store the software 32. The microcontroller 50 may also be coupled to an interface 53 which in turn may be coupled to the processor-based system 10. In this way, the systems 10 and 12 may exchange information such as the real time clock  
10 information, PIM information and signals to the processor-based system 10 and speaker 22.

In one embodiment of the present invention the system 12 may be a cellular telephone linked to the user's personal computer. On the opposite end of the spectrum,  
15 the system 12 may be a server. For example, the system 12 may be an Internet server. When the user is about to turn off his or her computer system 10, for example before going on a trip, the user's time sensitive data may automatically be transferred to a Web site operated by the server. The  
20 user can then access the time sensitive data, once stored on the Web site, from a computer different from the one originally used to record the PIM information.

Referring to Figure 6, the display 18, in accordance with one embodiment of the present invention, may display a  
25 graphical user interface 56 such as a calendar. Thus, a plurality of times may be displayed, with a particular

meeting time 58 highlighted. In one embodiment user definable information may be scrolled across the display 18. Thus, by exporting the time sensitive data from the application 16, the user is provided with a portion of his or her overall calendar and given a visual warning of the timed event.

Turning finally to Figure 7, the processor-based system 10 may include a processor 60 coupled to an interface 62 such as a bridge. The interface 62 may be coupled to a system memory 64 and a display controller 66. A display 68 may be coupled to a controller 66. An interface 70 may be coupled to the interface 62 as well as to a system bus 72. The real time clock may be part of the interface 70. The system bus 72 may be coupled to a storage medium 74, storing the software 76 for example that implements the PIM functions and the software 24.

In addition, the interface 70 may be coupled to a secondary bus 78 coupled to the interface 80. The interface 80 is coupled to the standby system 12 through its interface 53.

The secondary bus 78 may also couple a BIOS storage 82. The software 24 may be part of the BIOS software stored on the BIOS storage 82.

The storage 74 may define a time sensitive data queue, controlled for example by the application 24, for storing a plurality of time sensitive alerts in accordance with one

embodiment to the present invention. The queue may be in the form of a content addressable memory (CAM) with each location associated with a tag indicative of a particular time for action. The queue may be searched to find alerts  
5 having a tag indicative of a time that matches the current time, for example obtained from the real time clock.

The application 24 may be responsible for gathering time sensitive data from the application 16, prioritizing that data, ordering the data in the queue and running the  
10 queue. The application 24 may also notify the user when the last alert in the queue has been completed.

In some cases, the queue depth may be exceeded, for example because the number of timed entries exceeds the capacity of the queue. In such cases, a notification may  
15 be provided to the user that the queue depth has been exceeded and that additional entries may not be accepted. This may be accomplished for example, by a suitable graphical user interface.

In some embodiments of the present invention, it may  
20 advantageous to provide the ability to mute the audible notification so that the user is not disturbed when the user is in a meeting or the like. In such cases, the user may enter a command through the processor-based system 10 which mutes any audible alerts until the mute is released.  
25 In addition, it may be desirable to provide a interrupt which allows the user to enter a code into the processor-

based system 10 to turn off the on-going display, on the display 18, of an alert or upcoming timed event. In addition it may be desirable in some cases to turn off the display 18 at any time when the processor-based system 10 is operational to avoid duplicative indications of time sensitive data.

While the program 24 is illustrated as being an application program, it may also be implemented as part of an operating system. The program 24 may also be part of a personal information manager application as well.

While the present invention has been described with respect to a limited number of embodiments, those skilled in the art will appreciate numerous modifications and variations therefrom. It is intended that the appended claims cover all such modifications and variations as fall within the true spirit and scope of this present invention.

What is claimed is:

1           1.    A method comprising:  
2                automatically transferring time sensitive data  
3   from a storage coupled to a first processor-based system to  
4   a storage coupled to a second processor-based system; and  
5                automatically displaying said time sensitive data  
6   on a display coupled to said second processor-based system  
7   at a predetermined time.

1           2.    The method of claim 1 wherein said time sensitive  
2   data is automatically transferred from a storage when it is  
3   determined that the first processor-based system is being  
4   powered off.

1           3.    The method of claim 1 including automatically  
2   transferring personal information manager information.

1           4.    The method of claim 3 wherein automatically  
2   transferring personal information manager information  
3   includes automatically transferring timed alerts.

1           5.    The method of claim 1 including automatically  
2   providing an audible alert at a predetermined time.

1           6.    The method of claim 1 including providing real  
2   time clock information from said first processor-based  
3   system to said second processor-based system.

1           7.    The method of claim 1 including automatically  
2   displaying a portion of a calendar graphical user  
3   interface.

1           8.    An article comprising a medium for storing  
2   instructions that cause a processor-based system to:  
3                automatically transfer time sensitive data from a  
4   storage coupled to a first processor-based system to a  
5   storage coupled to a second processor-based system; and  
6                automatically display said time sensitive data on  
7   a display coupled to said second processor-based system at  
8   predetermined time.

1           9.    The article of claim 8 further storing  
2   instructions that cause a processor-based system to  
3   automatically transfer data from a storage when it is  
4   determined that the first processor-based system is being  
5   powered off.

1           10.   The article of claim 8 further storing  
2   instructions that cause a processor-based system to  
3   automatically transfer personal information manager  
4   information.



1           11. The article of claim 10 further storing  
2 instructions that cause a processor-based system to  
3 automatically transfer timed alerts.

1           12. The article of claim 8 further storing  
2 instructions that cause a processor-based system to  
3 automatically provide an audible alert at a predetermined  
4 time.

1           13. The article of claim 8 further storing  
2 instructions that cause a processor-based system to provide  
3 real time clock information from said first processor-based  
4 system to said second processor-based system.

1           14. The article of claim 8 further storing  
2 instructions that cause a processor-based system to  
3 automatically display a portion of the calendar graphical  
4 user interface.

1           15. A processor-based system comprising:  
2               a processor;  
3               a first storage storing a personal information  
4 manager application; and  
5               a second storage storing software including  
6 instructions that cause the processor to automatically

7 transfer time sensitive data to another processor-based  
8 device.

1 16. The system of claim 15 including a link on said  
2 system to said device.

1 17. The system of claim 16 wherein said system is a  
2 portable computer that includes said device.

1 18. The system of claim 17 including a display for  
2 said device and a housing for said computer, said display  
3 being located on the outside of said housing.

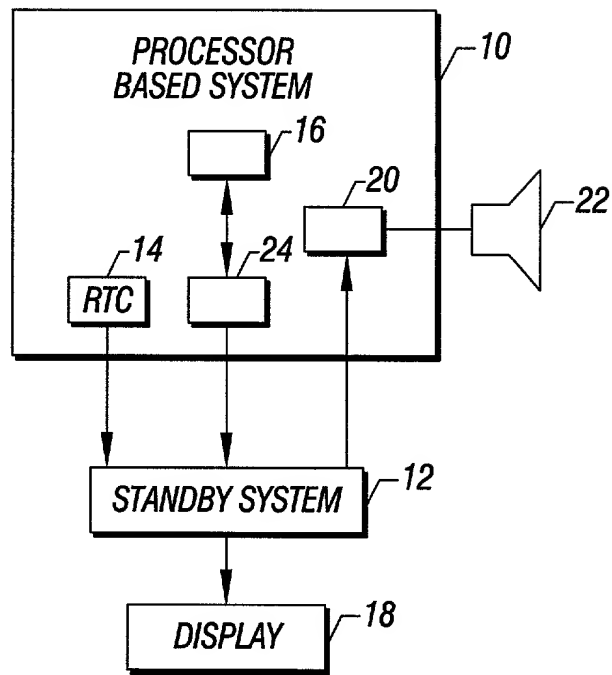
1 19. The system of claim 17 wherein said device  
2 receives clock information from said system.

1 20. The system of claim 15 wherein said processor  
2 automatically transfers said data to said device when the  
3 processor detects that the system will be turned off.

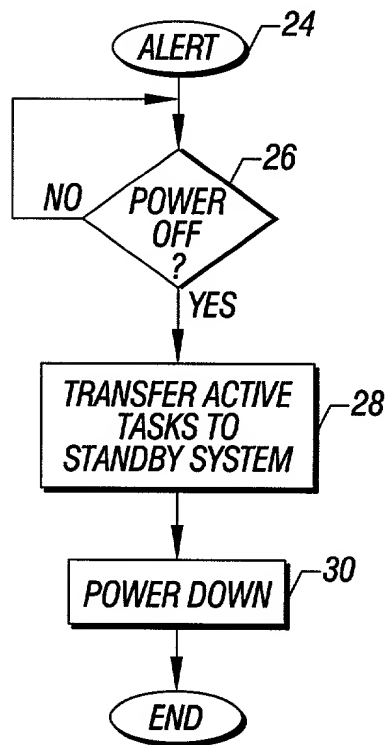
AUTOMATICALLY ENABLING INFORMATION TO BE DISPLAYED  
AFTER A PROCESSOR-BASED SYSTEM IS TURNED OFF

Abstract of the Disclosure

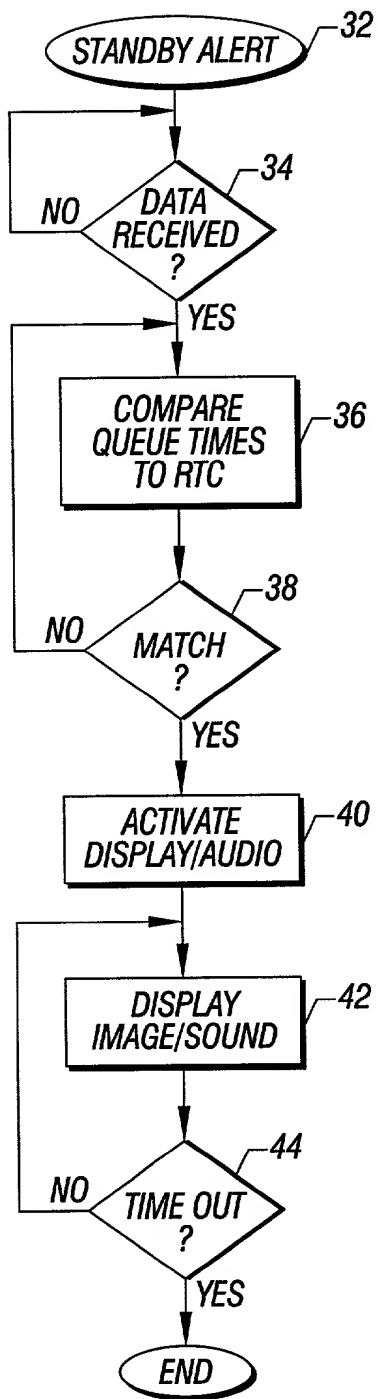
A system enables personal information manager (PIM) applications to continue to provide alerts and other time sensitive information even when the system upon which the PIM is stored is turned off. Automatically data may be transferred from a first processor-based system to a second processor-based system to implement time sensitive activities. The second processor-based system may provide a user notification at a predetermined time preset on the first processor-based system.



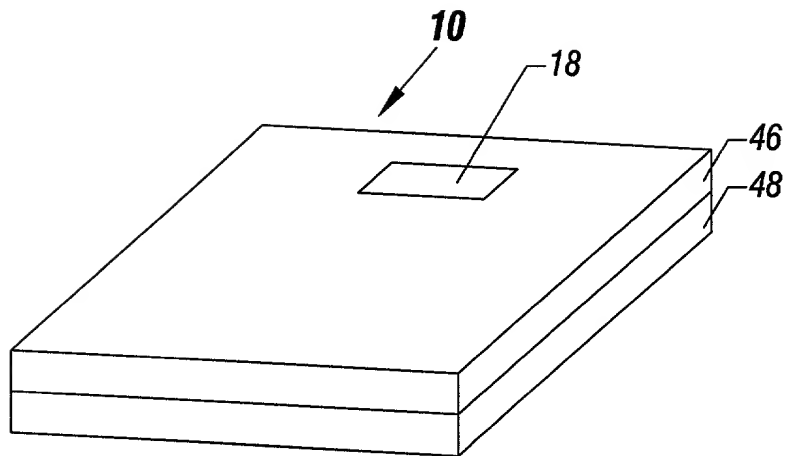
**FIG. 1**



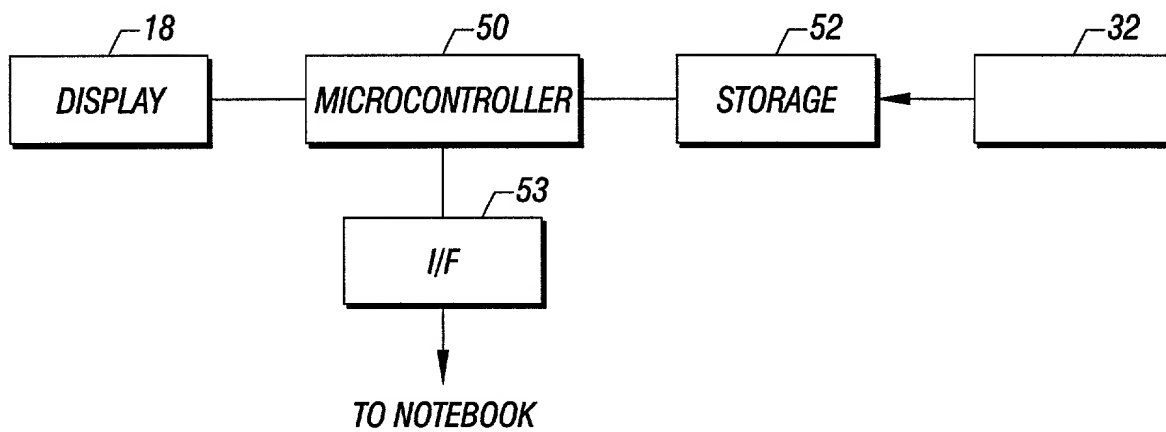
**FIG. 2**



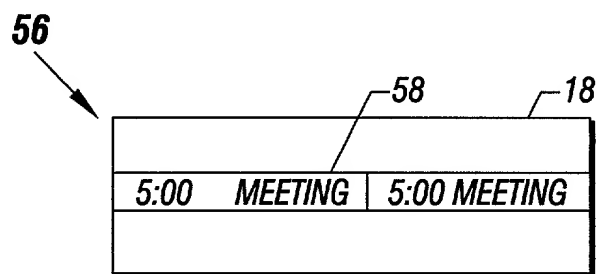
**FIG. 3**



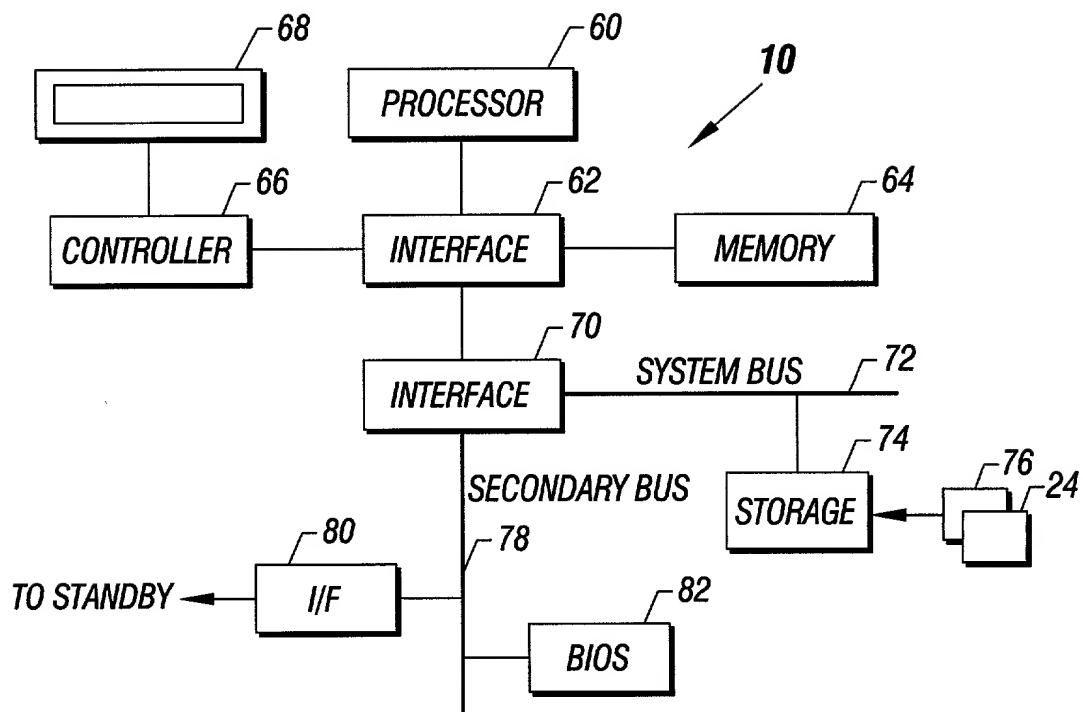
**FIG. 4**



**FIG. 5**



**FIG. 6**



**FIG. 7**

DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below, next to my name.

I believe I am the original, first, and sole inventor (if only one name is listed below) or an original, first, and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

AUTOMATICALLY ENABLING INFORMATION TO BE DISPLAYED  
AFTER A PROCESSOR-BASED SYSTEM IS TURNED OFF

the specification of which

X	is attached hereto.
	was filed on _____ as
	United States Application Number _____
	or PCT International Application Number _____
	and was amended on _____
	(if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claim(s), as amended by any amendment referred to above. I do not know and do not believe that the claimed invention was ever known or used in the United States of America before my invention thereof, or patented or described in any printed publication in any country before my invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, and that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve months (for a utility patent application) or six months (for a design patent application) prior to this application.

I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d), of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s):			Priority Claimed	
Number	(Country)	(Day/Month/Year Filed)	Yes	No
Number	(Country)	(Day/Month/Year Filed)	Yes	No



Number	(Country)	(Day/Month/Year Filed)	Yes	No
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I hereby claim the benefit under title 35, United States Code, Section 119(e) of the United States provisional application(s) listed below:

(Application Number)	(Filing Date)
(Application Number)	(Filing Date)


I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, Section 112, I acknowledge the duty to disclose all information known to me to be material to patentability as defined in Title 37, Code of Federal regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application:

(Application Number)	Filing Date	(Status-patented, pending, abandoned)
(Application Number)	Filing Date	(Status-patented, pending, abandoned)

I hereby appoint Timothy N. Trop, Reg. No. 28,994; Fred G. Pruner, Jr., Reg. No. 40,779, Dan C. Hu, Reg. No. 40,025; Coe F. Miles, Reg. No. 38,559, and John R. Merklings, Reg. No. 31,716 my patent attorneys, of TROP, PRUNER, HU & MILES, P.C., with offices located at 8554 Katy Freeway, Ste. 100, Houston, TX 77024, telephone (713) 468-8880, and Joseph R. Bond, Reg. No. 36,458; Richard C. Calderwood, Reg. No. 35,468; Sean Fitzgerald, Reg. No. 32,027; David J. Kaplan, Reg. No. 41,105; Leo V. Novakoski, Reg. No. 37,198; Naomi Obinata, Reg. No. 39,320; Thomas C. Reynolds, Reg. No. 32,488; Steven P. Skabrat, Reg. No. 36,279; Howard A. Skaist, Reg. No. 36,008; Steven C. Stewart, Reg. No. 33,555; Raymond J. Werner, Reg. No. 34,752; and Charles K. Young, Reg. No. 39,425; my patent attorneys, of INTEL CORPORATION; with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith.

Send correspondence to Timothy N. Trop, TROP, PRUNER, HU & MILES, P.C., 8554 Katy Freeway, Ste. 100, Houston, TX 77024 and direct telephone calls to Timothy N. Trop, (713) 468-8880.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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